



APPENDIX A

PATENT
5181-92401

DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or the below named inventors believe they are the original, first and joint inventors (if plural names are listed below) of the subject matter which is claimed in U.S. Patent No. 5,931,938, issued on August 3, 1999 and for which a reissue patent is sought on the invention entitled MULTIPROCESSOR COMPUTER HAVING CONFIGURABLE HARDWARE SYSTEM DOMAINS, the specification of which:

☐ is attached herewith.

☒ was filed on August 1, 2001 as Reissue Application Serial No. 09/920,433.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

PRIOR FOREIGN APPLICATION(S)

			<u>Priority Claimed</u>
N/A			Yes/No
(Number)	(Country)	(Date Filed)	
N/A			Yes/No
(Number)	(Country)	(Date Filed)	

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose all information known to me to be material to the patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56, which become available between the filing date of the prior application and the national or PCT international filing date of this application:

N/A		Pending
(Application Serial No.)	(Filing Date)	(Status)
N/A		Pending
(Application Serial No.)	(Filing Date)	(Status)

I hereby claim the benefit under title 35, United States code §119(e) of any United States provisional application(s) listed below:

N/A	
(Application Serial No.)	(Filing Date)
N/A	
(Application Serial No.)	(Filing Date)

I verily believe the original patent to be wholly or partly inoperative or invalid for the reasons described below:

- ☐ by reason of a defective specification or drawing.
☒ by reason of the patentee claiming more or less than he had the right to claim in the patent.
☐ by reason of other errors.

At least one error upon which the reissue is based is described below. If the reissue is a broadening reissue, such must be stated with an explanation as to the nature of the broadening:

I believe the original patent did not include claims directed to subject matter which I have the right to claim in the patent. Specifically, I believe I had a right to claim the subject matter of claim 1 without the following limitation: "a computer controller responsive to said commands for specifying to said domain configurator which of said system units belong to each of said hardware domains." The absence of a broader claim drawn to such subject matter represents an "error" in the original patent. Accordingly, new claim 40 filed with the reissue application recites the limitations of claim 1 without at least the above-recited language.

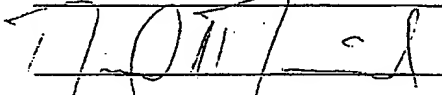
All errors which are being corrected in the present reissue application up to the time of filing of this declaration arose without any deceptive intention on the part of the applicant.

Please direct all communications as follows:

B. Noël Kivlin
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I hereby declare that all statements made of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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Inventor's Signature:  Date: 6/27/02

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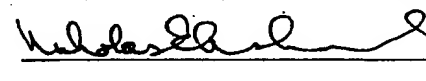
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Inventor's Signature:  Date: 6-27-02

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**APPENDIX B-1: Comparison of Claim 1 of Original Application and Claim 40 in the
Current Reissue Application**

Application Claim 1 Language	Reissue Claim 40 Language
A multiprocessor computer having hardware domains variably configurable by commands from an operator, said computer comprising:	A multiprocessor computer having hardware domains [variably] <u>variable</u> configurable by commands from an operator, said computer comprising:
a plurality of separate system units for performing sequences of transactions, each said system unit being individually physically removable and replaceable within said computer, and each including at least one of	[a] plurality of separate system units for performing sequences of transactions, each [said system unit being individually physically removable and replaceable within said computer, and each] including at least one of:
a processor unit for generating addresses within a predetermined global range,	[a] processor unit for generating addresses within a predetermined global range,
a memory unit for storing data at a set of addresses within said predetermined global range,	a memory unit for storing data at a set of addresses within said predetermined global range,
an input/output adapter for generating and/or receiving a set of addresses within said predetermined global range;	an input/output adapter for generating and/or receiving a set of addresses within said predetermined global range;
a global address router coupled to said system units for transferring addresses generated in any of said system units to others of said system units;	a global address router coupled to said system units for transferring addresses generated in any of said system units to others of said system units;
a global data router for transferring data from any of said system units to others of said system units;	a global data router for transferring data from any of said system units to others of said system units;
a control-signal distributor for communicating a plurality of control signals from any of said system units to others of said system units for affecting the operation of all of said system	a control-signal distributor for communicating a plurality of control signals from any of said system units to others of said system units for affecting the operation of all of said system

units in response to conditions occurring in said any system unit;	units in response to conditions occurring in said any system unit;
a domain configurator for electronically dividing said computer into a plurality of software-configurable hardware domains each comprising an arbitrary subset of said system units independently of any physical reconnection of said system units within said computer;	a domain configurator for electronically dividing said computer into a plurality of software-configurable hardware domains each comprising an arbitrary subset of said system units independently of any physical reconnection of said system units within said computer; <u>and</u>
a computer controller responsive to said commands for specifying to said domain configurator which of said system units belong to each of said hardware domains;	
a domain filter coupled to all of said system units for electronically inhibiting at least some of said control signals originating in those of said system units within one of said domains from affecting certain of said system units outside said one domain.	a domain filter coupled to all of said system units for electronically inhibiting at least some of said control signals originating in those of said system units within one of said domains from affecting certain of said system units outside said one domain, <u>wherein said domain filter is coupled to at least one of said global routers for inhibiting transactions on said one global router originating in those of said system units within one of said domains from being received in certain of said system units outside said one domain.</u>

**APPENDIX B-2: Comparison of Claim 1 of Original Application and Claim 57 in the
Current Reissue Application**

Application Claim 1 Language	Reissue Claim 57 Language
A multiprocessor computer having hardware domains variably configurable by commands from an operator, said computer comprising:	A [multiprocessor] computer [having hardware domains variably configurable by commands from an operator, said] <u>system</u> comprising:
a plurality of separate system units for performing sequences of transactions, each said system unit being individually physically removable and replaceable within said computer, and each including at least one of	a plurality of separate system units for performing sequences of transactions, [each said system unit being individually physically removable and replaceable within said computer, and] each including at least [one of]
a processor unit for generating addresses within a predetermined global range,	a processor unit for generating addresses within a predetermined global range;
a memory unit for storing data at a set of addresses within said predetermined global range,	
an input/output adapter for generating and/or receiving a set of addresses within said predetermined global range;	
a global address router coupled to said system units for transferring addresses generated in any of said system units to others of said system units;	a global address router coupled to said <u>plurality of separate</u> system units <u>and configured to transfer</u> [transferring] addresses generated in any of said system units to others of said system units;
a global data router for transferring data from any of said system units to others of said system units;	
a control-signal distributor for communicating a plurality of control signals from any of said system units to others of said system units for affecting the operation of all of said system	a control-signal distributor [for] <u>configured to</u> [communicating] <u>communicate</u> a plurality of control signals from any of said system units to others of said system units for affecting the operation of all of said system units in response

units in response to conditions occurring in said any system unit;	to conditions occurring in said any system unit;
a domain configurator for electronically dividing said computer into a plurality of software-configurable hardware domains each comprising an arbitrary subset of said system units independently of any physical reconnection of said system units within said computer;	a domain configurator [for] <u>coupled to electronically [dividing] partition said [computer into]a plurality of system units into one or more software-configurable hardware domains independently of any physical reconnection of said plurality of system units, wherein each [comprising]of said one or more software-configurable hardware domains includes an arbitrary subset of said plurality of system units [within said computer] ; and</u>
a computer controller responsive to said commands for specifying to said domain configurator which of said system units belong to each of said hardware domains;	
a domain filter coupled to all of said system units for electronically inhibiting at least some of said control signals originating in those of said system units within one of said domains from affecting certain of said system units outside said one domain.	a domain filter coupled to [all] <u>each of said plurality of system units [for] and configured to electronically inhibit[ing] at least some of said control signals originating in [those of said] system units [within] belonging to a given one of said hardware domains from affecting [certain of said] system units [outside said one]not belonging to said given one of said hardware domains, wherein said domain filter is further coupled to inhibit transactions originating in system units belonging to said given one of said hardware domains from being received in said system units not belonging to said given one of said hardware domains.</u>

**APPENDIX B-3: Comparison of Claim 1 of Original Application and Claim 50 in the
Current Reissue Application**

Application Claim 1 Language	Reissue Claim 50 Language
A multiprocessor computer having hardware domains variably configurable by commands from an operator, said computer comprising:	A multiprocessor computer having hardware domains variably configurable by commands from an operator, said computer comprising:
a plurality of separate system units for performing sequences of transactions, each said system unit being individually physically removable and replaceable within said computer, and each including at least one of	a plurality of separate system units for performing sequences of transactions, each [said system unit being individually physically removable and replaceable within said computer, and each] including at least one of:
a processor unit for generating addresses within a predetermined global range,	[a] processor unit for generating addresses within a predetermined global range,
a memory unit for storing data at a set of addresses within said predetermined global range,	a memory unit for storing data at a set of addresses within said predetermined global range,
an input/output adapter for generating and/or receiving a set of addresses within said predetermined global range;	an input/output adapter for generating and/or receiving a set of addresses within said predetermined global range;
a global address router coupled to said system units for transferring addresses generated in any of said system units to others of said system units;	a global address router coupled to said system units for transferring addresses generated in any of said system units to others of said system units;
a global data router for transferring data from any of said system units to others of said system units;	a global data router for transferring data from any of said system units to others of said system units;
a control-signal distributor for communicating a plurality of control signals from any of said system units to others of said system units for affecting the operation of all of said system units in response to conditions occurring in said	a control-signal distributor for communicating a plurality of control signals from any of said system units to others of said system units for affecting the operation of all of said system units in response to conditions occurring in said any system unit;

any system unit;	
a domain configurator for electronically dividing said computer into a plurality of software-configurable hardware domains each comprising an arbitrary subset of said system units independently of any physical reconnection of said system units within said computer;	a domain configurator for electronically dividing said computer into a plurality of software-configurable hardware domains each comprising an arbitrary subset of said system units independently of any physical reconnection of said system units within said computer, <u>said domain configurator further combining a plurality of said hardware domains into a domain cluster comprising an arbitrary subset of said domains independently of any physical reconnection of said system units within said computer;</u>
a computer controller responsive to said commands for specifying to said domain configurator which of said system units belong to each of said hardware domains;	
a domain filter coupled to all of said system units for electronically inhibiting at least some of said control signals originating in those of said system units within one of said domains from affecting certain of said system units outside said one domain.	a domain filter coupled to all of said system units for electronically inhibiting at least some of said control signals originating in those of said system units within one of said domains from affecting certain of said system units outside said one domain, <u>said domain filter permitting said at least some control signals originating in those of said system units within said one domain to affect those of said systems units outside said one domain but within said domain cluster.</u>

**APPENDIX B-4: Comparison of Claim 36 of Original Application and Claim 53 in the
Current Reissue Application**

Application Claim 36 Language	Reissue Claim 53 Language
A multiprocessor computer having hardware domains variably configurable by commands from an operator, said computer comprising:	A multiprocessor computer having hardware domains variably configurable by commands from an operator, said computer comprising:
a plurality of separate system units for performing sequences of transactions, each said system unit being individually physically removable and replaceable within said computer, and each including at least one of:	a plurality of separate system units for performing sequences of transactions, [each said system unit being individually physically removable and replaceable within said computer, and] each including at least one of:
processor unit for generating addresses within a predetermined global range,	processor unit for generating addresses within a predetermined global range,
a memory unit for storing data at a set of addresses within said predetermined global range,	a memory unit for storing data at a set of addresses within said predetermined global range,
an input/output adapter for generating and/or receiving a set of addresses within said predetermined global range;	an input/output adapter for generating and/or receiving a set of addresses within said predetermined global range;
a global address router coupled to said system units for transferring addresses generated in any of said system units to others of said system units;	a global address router coupled to said system units for transferring addresses generated in any of said system units to others of said system units;
a global data router for transferring data from any of said system units to others of said system units;	a global data router for transferring data from any of said system units to others of said system units;
a control-signal distributor for communicating a plurality of control	a control-signal distributor for communicating a plurality of control

signals from any of said system units to all others of said system units for affecting the entire operation of all of said system units in response to error and status conditions occurring in said any system unit;	signals from any of said system units to all others of said system units for affecting the entire operation of all of said system units in response to error and status conditions occurring in said any system unit;
a domain configurator for electronically dividing said computer into a plurality of software-configurable hardware domains each comprising an arbitrary subset of said system units independently of any physical reconnection of said system units within said computer;	a domain configurator for electronically dividing said computer into a plurality of software-configurable hardware domains each comprising an arbitrary subset of said system units independently of any physical reconnection of said system units within said computer;
a computer controller responsive to said commands for specifying to said domain configurator which of said system units belong to of said hardware domains;	
a domain filter coupled to all of said system units for electronically inhibiting at least some of said control signals originating in those of said system units within one of said domains from affecting certain of said system units outside said one domain.	a domain filter coupled to all of said system units for electronically inhibiting at least some of said control signals originating in those of said system units within one of said domains from affecting certain of said system units outside said one domain.

**APPENDIX B-5: Comparison of Claim 14 of Original Application and Claim 63 in the
Current Reissue Application**

Application Claim 14 Language	Reissue Claim 63 Language
A method of partitioning a computer having a plurality of system units, a global address router, a global data router, a control-signal distributor, and a domain filter into a plurality of independent hardware domains under programmable control, comprising:	A method of partitioning a computer <u>system</u> having a plurality of system units [, a global address router, a global data router, a control-signal distributor, and a domain filter] into [a plurality of] <u>one or more</u> independent hardware domains [under programmable control], <u>said method</u> comprising:
(a) starting a configuration mode;	
(b) receiving specification data defining a subset of said system units for inclusion within one of said hardware domains;	[(b)] receiving specification data defining a <u>first</u> subset of said <u>plurality of</u> system units for inclusion within [one of said] <u>a</u> first hardware domain;
(c) loading said specification data into a domain filter so as to render those of said system units within said one domain responsive to certain control signals in said distributor, and to render others of said system units unresponsive to said distributor;	[(c)] loading said specification data into a domain filter [so as to render those of said] <u>and causing each</u> system unit [within said one domain] <u>belonging to said first subset of said plurality of system units to be responsive to</u> [certain] <u>a first set of</u> control signals [in said distributor, and [to render others] <u>to cause system units not belonging to said first subset of said plurality of</u> [said] system units <u>to be unresponsive to said first set of control signals;</u> and
(d) repeating steps (b) and (c) for further specification data defining a different subset of said system units,	
Wherein step (c) is also responsive to said specification data for loading said domain filter so as to render those of said system units within	[Wherein step (c) is also responsive to said] <u>in response to said loading said specification data [for loading]into</u> said domain filter [so as to

said one domain responsive to addresses on said global address router originating from those of said system units within said one domain, and to render said system units within said first domain unresponsive to addresses on said global address router originating from at least some of those of said system units not within the first domain.

render those of said] causing each system unit[s within said one domain] belonging to said first subset of said plurality of system units to be responsive to addresses [on said global address router]originating from [those of] said system units [within said one domain]belonging to said first subset of said plurality of system units, and [to render said]causing each system unit [within said first domain] belonging to said first subset of said plurality of system units to be unresponsive to addresses [on said global address router] originating from {at least some of those of} said system units not [within the first domain] belonging to said first subset of said plurality of system units.